



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Hayashida et al.**

Serial No.: **09/939,870**

Filed: **August 27, 2001**

Title: **Semiconductor Integrated Circuit
Device and Method for Designing the
Same**

Attorney Docket No.: **N26180400W**

Group Art Unit: **2836**

Examiner: **Nguyen, D.**

**AMENDMENT AFTER FINAL ACTION
(37 C.F.R. §1.116)**

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

A. Introductory Comments

The following is submitted in response to the FINAL Office Action dated August 22, 2003, and is currently due December 22, 2003 with a one month extension.

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12/29/2003 10:00 AM ANDAF1 00000098 09939870

02 FC:1201
03 FC:1999

86.00 OP
82.00 OP

37 C.F.R. §1.8

15 I hereby certify that this correspondence is being

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Date of Deposit: December 19, 2003

25 Typed/Printed Name: Darryl Walker

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